

## SCHEMATICS

for

MAN-COMPUTER INTERACTIVE DATA  
ACCESS SYSTEM (McIDAS)

Final Report on Contract NAS5-23296

BOOK 4

(NASA-CR-143823) SCHEMATICS FOR  
MAN-COMPUTER INTERACTIVE DATA ACCESS SYSTEM  
(MCIDAS), BOOK 4 Final Report (Wisconsin  
Univ.) 99 p HC \$4.75 CSCI 09

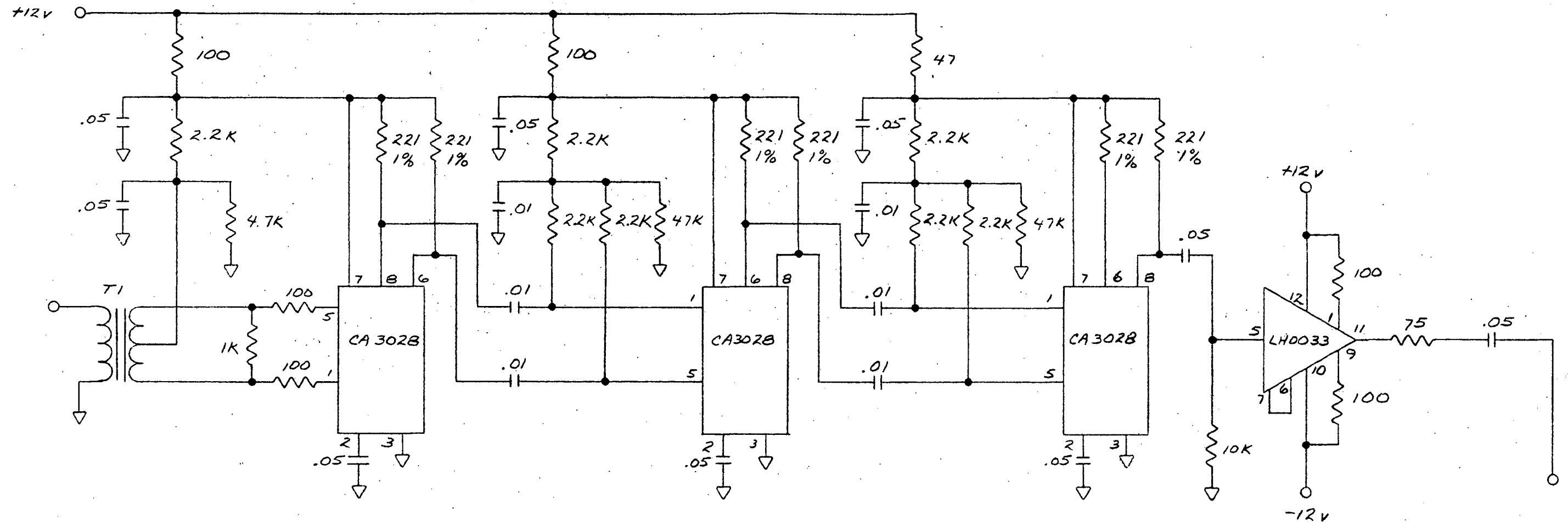
N75-28737

CSCI 09B

Unclas  
31276

G3/60

REVISIONS			
SYM	DESCRIPTION	DATE	APPROVED



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FOLDOUT FRAME 1

THE UNIVERSITY OF WISCONSIN			
SPACE SCIENCE & ENGINEERING CENTER			
MADISON, WISCONSIN			
TITLE			
RF LIMITER			
SCALE	DRAFTSMAN	DATE	CHECKER
NONE	AW	4-3-75	
APPROVAL	DATE	DESIGN ACTIVITY	APPROVAL
PROJECT NO.	SIZE	DRAWING N	
7001	B	SHEET 1 OF 1	

RF Limiter



NOTES:

- ① (N.U.)  $\equiv$  SIGNAL NOT USED
- ② ALL RESISTORS  $\pm 5\%$  UNLESS OTHERWISE STATED

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OF POOR QUALITY

### FOLDOUT FRAME

FOLDOUT FRAME

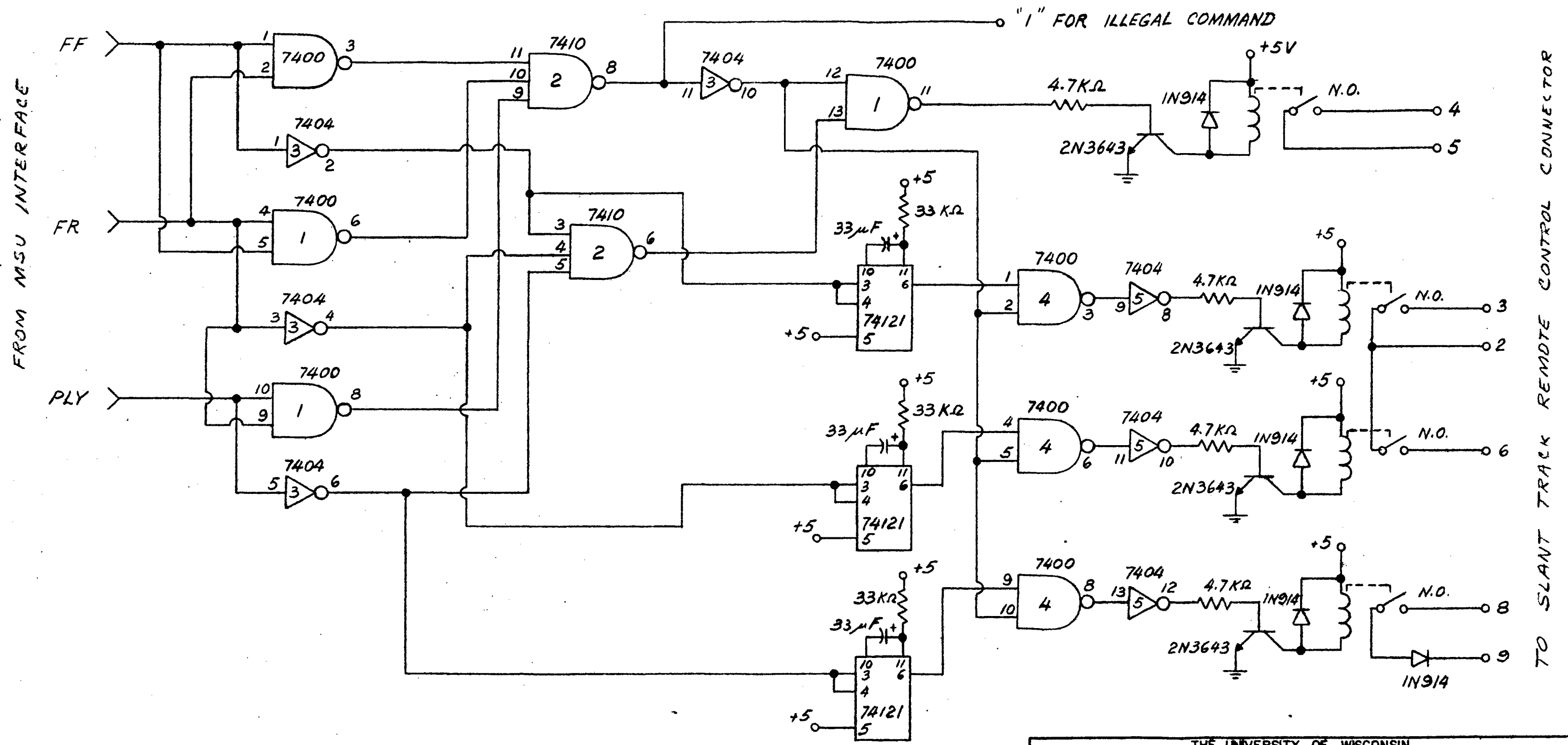
THE UNIVERSITY OF WISCONSIN	
SPACE SCIENCE & ENGINEERING CENTER	
TITLE	
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SCALE	DRAFTSMAN <i>N/A</i>
DATE	CHECKER <i>5/25/74</i>
DATE	ENGINEER
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NEXT HIGHER ASSEMBLY	PRODUCT ASSURANCE <i>2/2/74</i>
PROJECT NO.	SIZE SHEET <i>1</i> OF <i>1</i>
DRAWING	Demodulator







REVISIONS			
SYM	DESCRIPTION	DATE	APPROVED



THE UNIVERSITY OF WISCONSIN					
SPACE SCIENCE & ENGINEERING CENTER					
MADISON, WISCONSIN					
TITLE					
MSU RELAY INTERFACE					
SCALE	DRAFTSMAN	DATE	CHECKER	DATE	ENGINEER
NONE	N.K.M.	6/26/72			
APPROVAL		DATE	DESIGN ACTIVITY	APPR	
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MSU Relay Interface



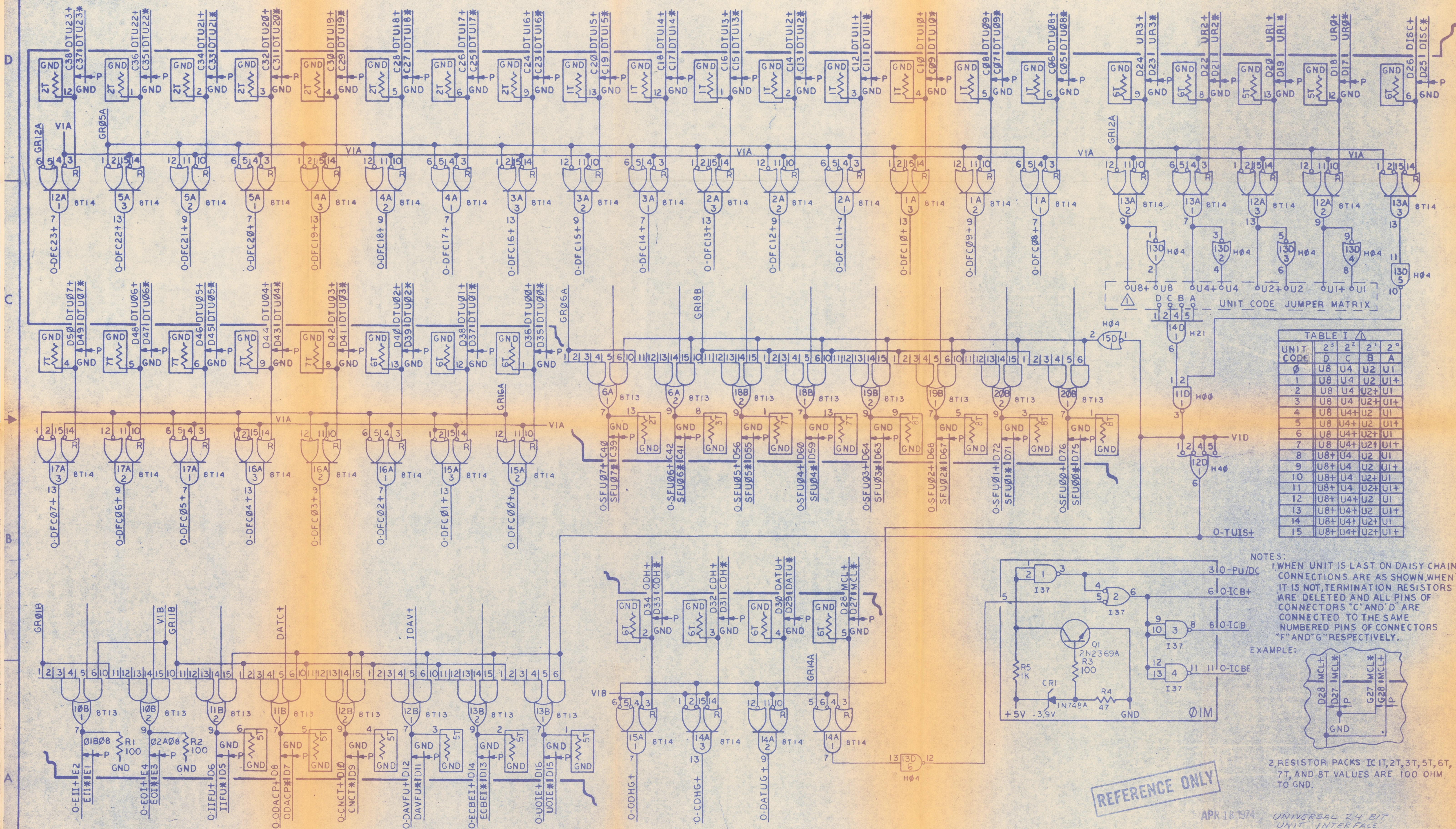
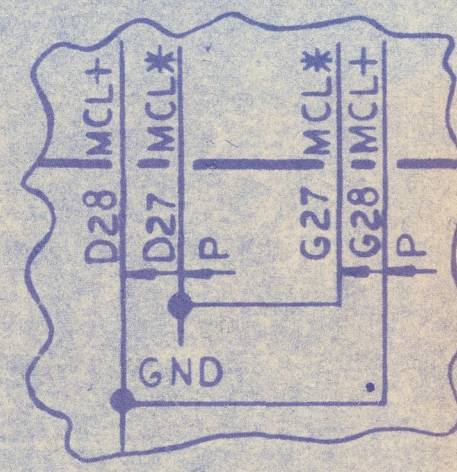


TABLE I

UNIT CODE	2 <sup>3</sup>	2 <sup>2</sup>	2 <sup>1</sup>	2 <sup>0</sup>
0	U8	U4	U2	U1
1	U8	U4	U2	U1+
2	U8	U4	U2+	U1
3	U8	U4	U2+	U1+
4	U8	U4+	U2	U1
5	U8	U4+	U2	U1+
6	U8	U4+	U2+	U1
7	U8	U4+	U2+	U1+
8	U8+	U4	U2	U1
9	U8+	U4	U2	U1+
10	U8+	U4	U2+	U1
11	U8+	U4	U2+	U1+
12	U8+	U4+	U2	U1
13	U8+	U4+	U2	U1+
14	U8+	U4+	U2+	U1
15	U8+	U4+	U2+	U1+

NOTES:  
 1. WHEN UNIT IS LAST ON DAISY CHAIN, CONNECTIONS ARE AS SHOWN. WHEN IT IS NOT, TERMINATION RESISTORS ARE DELETED AND ALL PINS OF CONNECTORS "C" AND "D" ARE CONNECTED TO THE SAME NUMBERED PINS OF CONNECTORS "F" AND "G" RESPECTIVELY.

EXAMPLE:

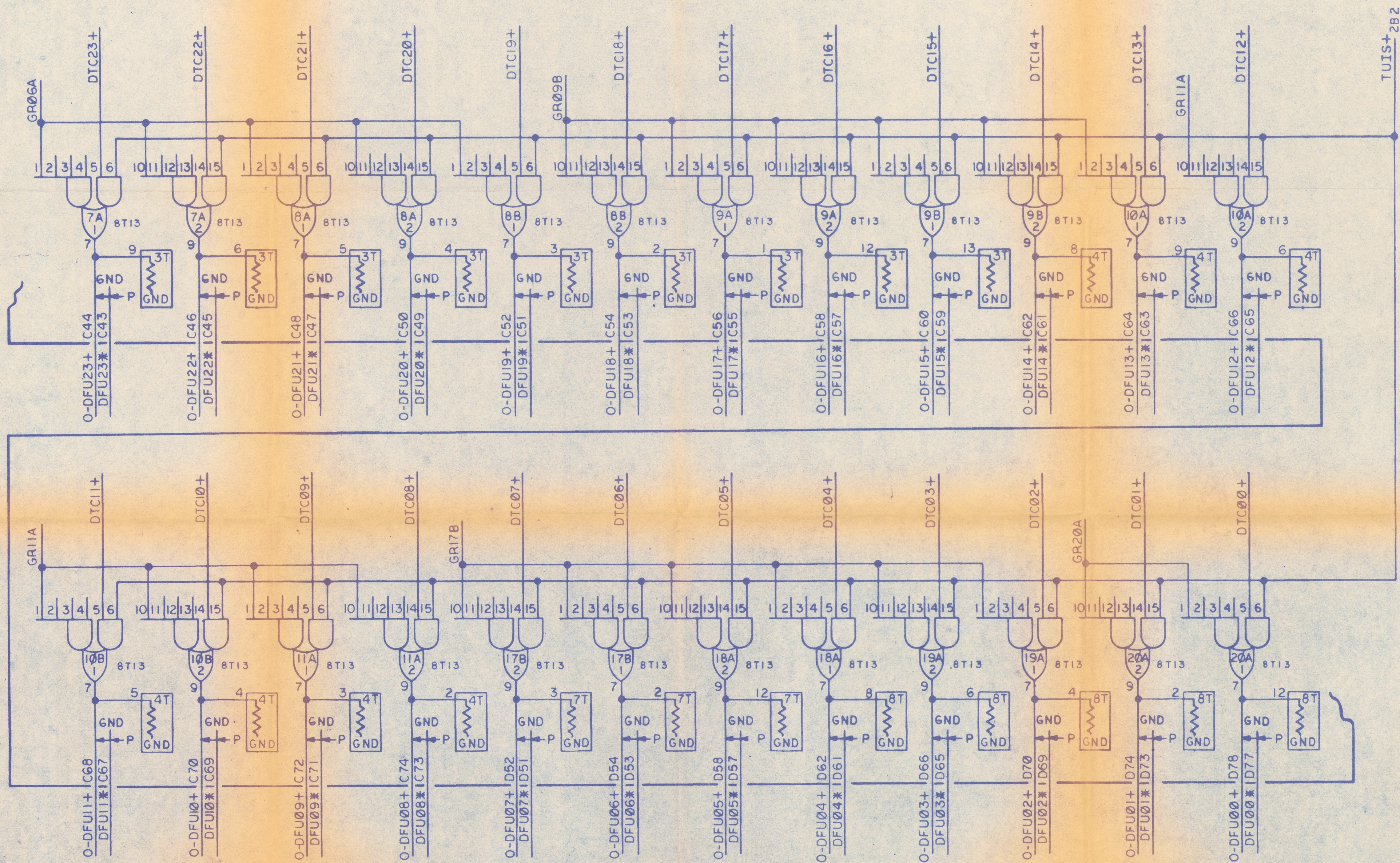


2. RESISTOR PACKS IC1, 2T, 3T, 5T, 6T, 7T, AND 8T VALUES ARE 100 OHM TO GND.

REFERENCE ONLY

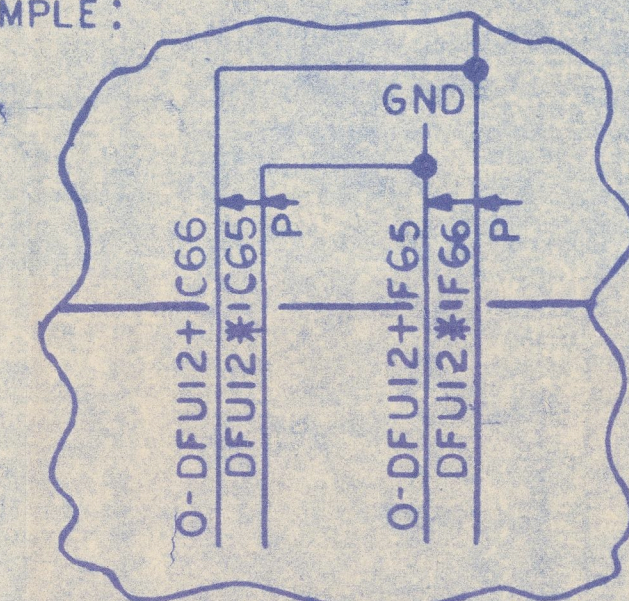
APR 18 1974 UNIVERSAL 24 BIT UNIT INTERFACE





NOTES:  
1. WHEN UNIT IS LAST ON DAISY CHAIN, CONNECTIONS ARE AS SHOWN. WHEN IT IS NOT, TERMINATION RESISTORS ARE DELETED AND ALL PINS OF CONNECTORS "C" AND "D" ARE CONNECTED TO THE SAME NUMBERED PINS OF CONNECTORS "F" AND "G" RESPECTIVELY.

EXAMPLE:



2. RESISTOR PACKS IC3T, 4T, 7T, AND 8T VALUES ARE 100 OHMS TO GND.

REFERENCE ONLY

APR 18 1974







UNIVERSAL 24 BIT UNIT INTERFACE

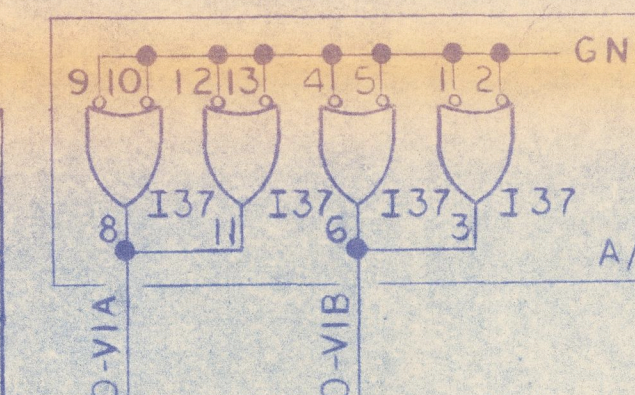
THIS DATA IS PROPRIETARY TO DATACRAFT CORPORATION AND SHALL NOT BE RELEASED OR DISCLOSED IN WHOLE OR IN PART FOR PURPOSES OTHER THAN THOSE AS PRESCRIBED IN ASPR 9-203. (b)(2)	SIZE	CODE IDENT NO.	DRAWING NO.	REV.
	D	29436	CS37000	
SCALE: NONE		CONT ON SHEET		

UNIVERSAL 24 BIT INTERFACE (5 Drawing Set)

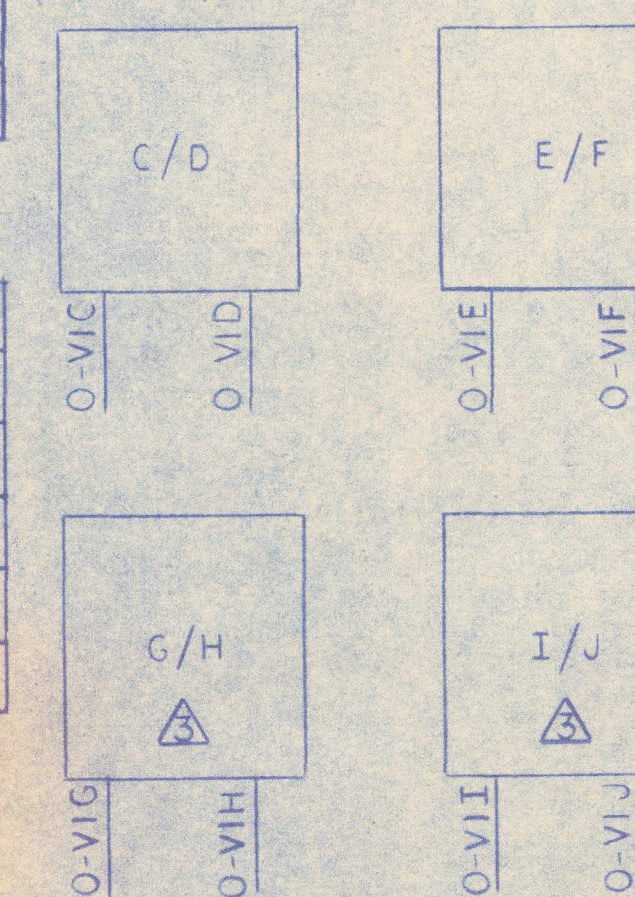


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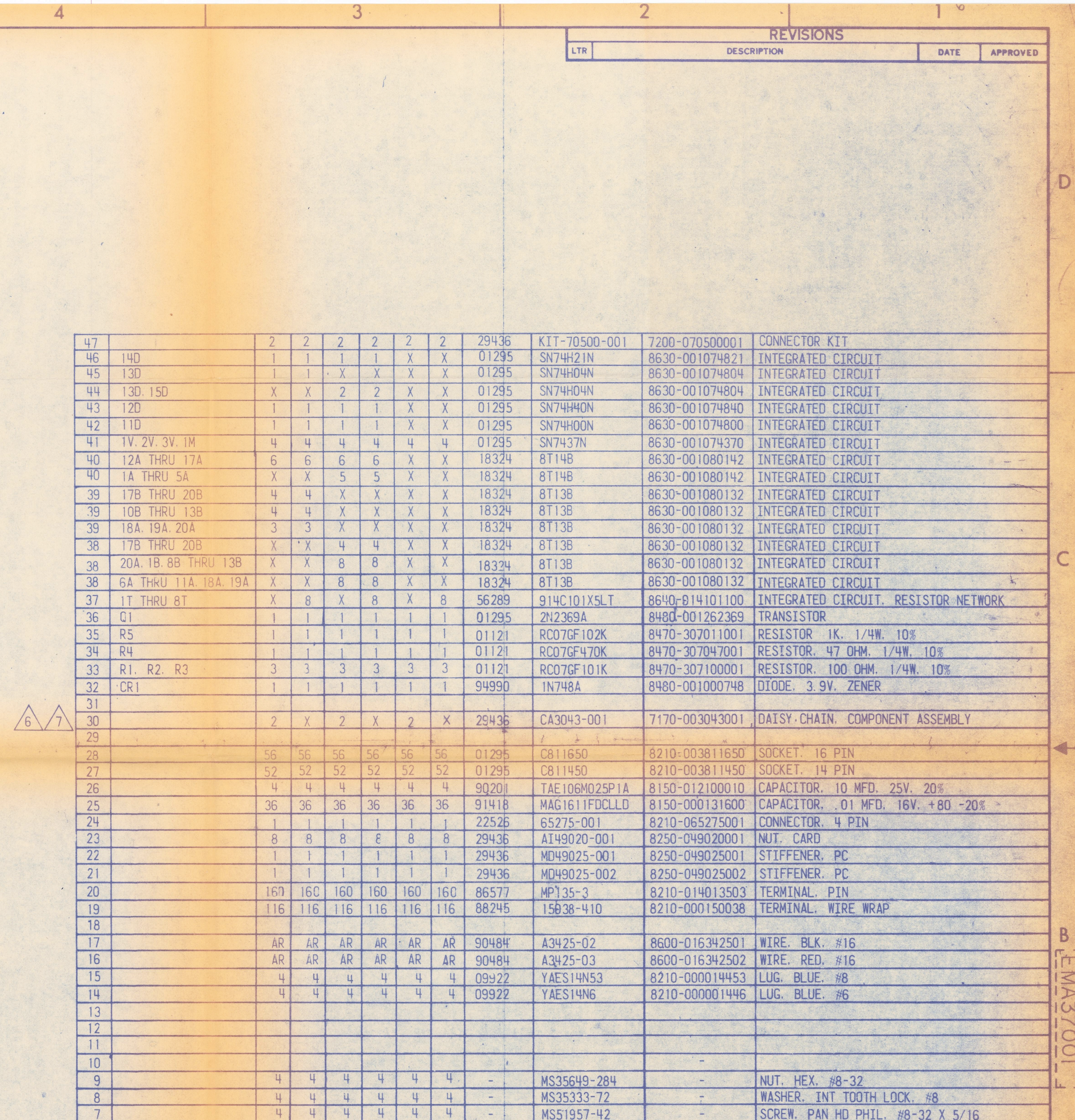


- NOTES:
1. UNLESS OTHERWISE SPECIFIED, ALL RESISTANCE VALUES ARE IN OHMS  $\pm 10\%$ , 1/8W. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
  2. PARTIAL REFERENCE DESIGNATIONS FOR INTEGRATED CIRCUITS ARE SHOWN FOR COMPLETE DESIGNATION PREFIX WITH "IC".
- △, OMIT THIS BLOCK FOR 3037 BOARD



ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED		CONTRACT NO.		Datacraft Corporation FORT LAUDERDALE, FLORIDA	
TOLERANCES X .1 ANGLE .1 XX .2 HOLES XXX .3 SURFACE FOURTHNESS		DRAFTSMAN <i>E. R. ...</i> 4/26/73 CHECKER <i>A. ...</i> 6/20/73		UNIVERSAL 24 BIT INTERFACE	
DEV NO. 21256		PROD. ENGR. <i>C. ...</i> 7-6-73		SIZE CODE IDENT. NO. DRAWING NO.	
MATERIAL		ENGINEER <i>W. ...</i> 7-6-73		D 29436 CS37000	
FINISH		PROJ. ENGR. <i>W. ...</i> 7-1-73		SCALE NONE CONT. ON SHEET	
APPLICATION		NEXT ASSY USED ON		REV	





6	IF ITEM 30 IS INSTALLED AT SYSTEM INTEGRATION REMOVE IC'S 05T THRU 08T
7	IF ITEM 30 IS INSTALLED AT SYSTEM INTEGRATION REMOVE IC'S 01T THRU 04T
8	WIRE LENGTH TO BE 60.0 ±1.0 MEASURED FROM CARD EDGE
9	WIRE LENGTH TO BE 60.0 ±1.0 MEASURED FROM CARD EDGE

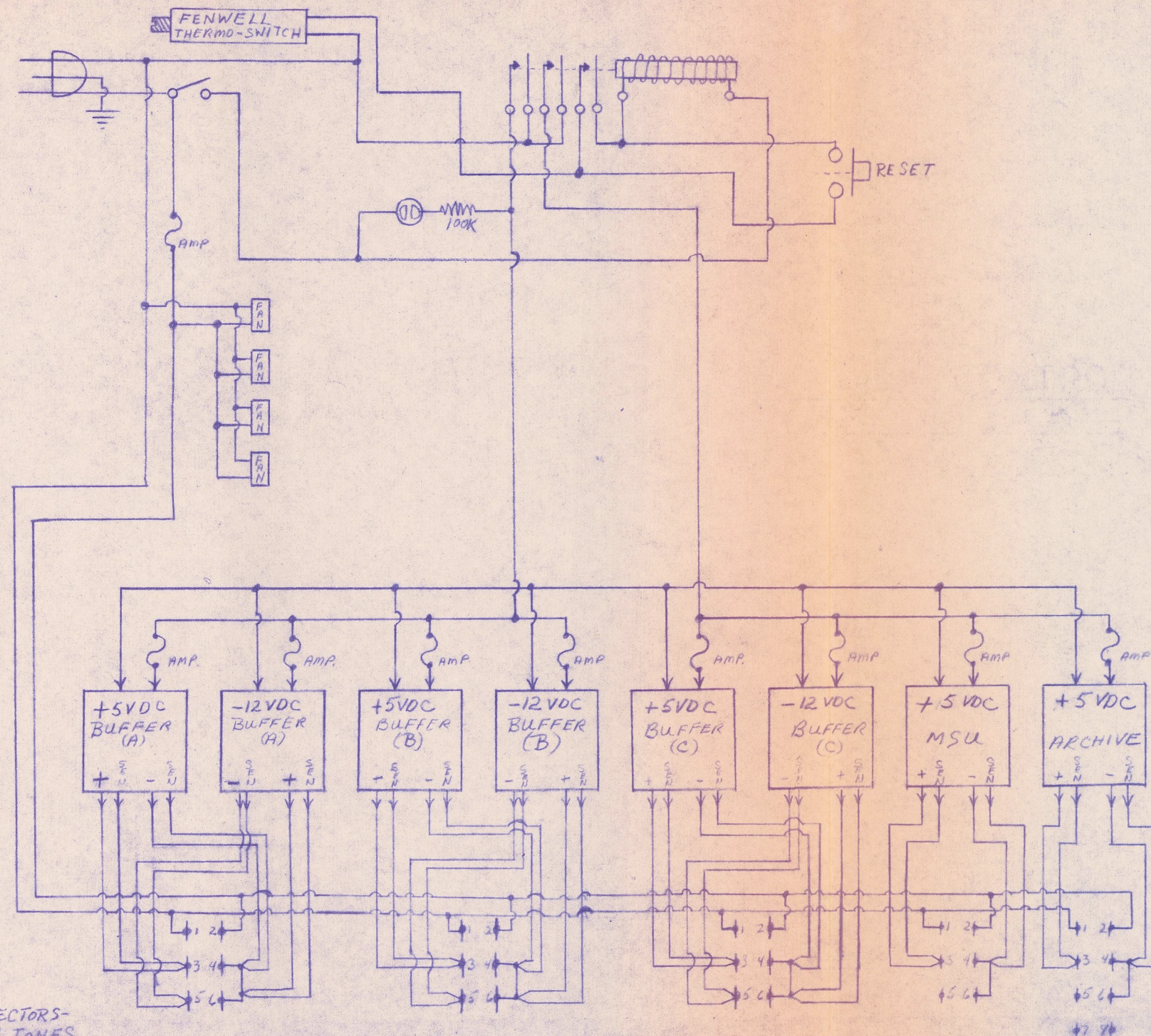
[illegible]

SCALE | TO | CONT ON SHEET — UNIVERSAL 24 BIT INTERFACE









CONNECTORS -  
CINCH JONES  
C&B PIN

REVISIONS			
SYM	DESCRIPTION	DATE	APPROVED

THE UNIVERSITY OF WISCONSIN					
SPACE SCIENCE & ENGINEERING CENTER					
MADISON, WISCONSIN					
TITLE					
ARCHIVE POWER BAY					
SCALE	DRAFTSMAN	DATE	CHECKER	DATE	ENGINEER
NONE	GMB	5-29-74			
APPROVAL		DATE	DESIGN ACTIVITY	APPROVA	DATE
PROJECT NO.		SIZE	SHEET		DRAWING
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